

CvPcb

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Contents

1	Introduction to CvPcb							
2	CvF	cb Fea	atures	2				
	2.1	Manua	l or Automatic Association	2				
3	Invoking CvPcb							
4	CvF	cb Co	mmands	3				
	4.1	Main S	Screen	3				
	4.2	Main V	Window Toolbar	4				
	4.3	Main V	Window Keyboard Commands	4				
	4.4	CvPcb	Configuration	5				
5	Foot	tprint	Libraries Management	6				
	5.1	Import	tant remark:	6				
	5.2	Footpr	int Library tables	6				
		5.2.1	Global Footprint Library Table	7				
		5.2.2	Project Specific Footprint Library Table	7				
		5.2.3	Initial Configuration	8				
		5.2.4	Adding Table Entries	8				
		5.2.5	Environment Variable Substitution	8				
		5.2.6	Using the GitHub Plugin	9				
		5.2.7	Usage Patterns	10				
	5.3	Using	the Footprint Library Table Wizard	12				
6	Viev	wing tl	he Current Footprint	15				
	6.1	The vi	ew footprint command	15				
		6.1.1	Status Bar Information	15				
		6.1.2	Keyboard Commands	15				
		6.1.3	Mouse Commands	16				
		6.1.4	Context Menu	16				
		6.1.5	Horizontal Toolbar	16				
		6.1.6	Vertical Toolbar	17				

	6.2	Viewing the Current 3D Model	17
		6.2.1 Mouse Commands	18
		6.2.2 Horizontal Toolbar	18
7	Usi	ng CvPcb to Associate Components with Footprints	19
	7.1	Manually Associating Footprints with Components	19
	7.2	Filtering the Footprint List	19
8	Aut	comatic Associations	23
	8.1	Equivalence files	23
	8.2	Equivalence File Format	23
	8.3	Automatically Associating Footprints to Components	24

Reference manual

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Feedback

Please direct any bug reports, suggestions or new versions to here:

- About KiCad document: https://gitlab.com/kicad/services/kicad-doc/issues
- About KiCad software: https://gitlab.com/kicad/code/kicad/issues
- About KiCad software i18n: https://gitlab.com/kicad/code/kicad-i18n/issues

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1 Introduction to CvPcb

CvPcb is a tool that allows you to associate components in your schematic to component footprints used when laying out the printed circuit board. This association is added to the net list file created by the schematic capture program Eeschema.

The net list file generated by Eeschema specifies which printed circuit board footprint is associated with each component in the schematic only when the footprint field of the component is initialized.

This is the case when component footprints are associated during schematic capture by setting the component's footprint field, or it is set in the schematic library when loading the symbol.

CvPcb provides a convenient method of associating footprints to components during schematic capture. It provides footprint list filtering, footprint viewing, and 3D component model viewing to help ensure the correct footprint is associated with each component.

Components can be assigned to their corresponding footprints manually or automatically by creating equivalence files (.equ files). Equivalence files are lookup tables associating each component with it's footprint.

This interactive approach is simpler and less error prone than directly associating the footprints in the schematic editor.

CvPcb allows you to see the list of available footprints and to display them on the screen to ensure you are associating the correct footprint.

It can be run only from Eeschema, from the top toolbar, either when Eeschema is started from the KiCad project manager or when Eeschema is started as a stand alone application.

Running CvPcb from Eeschema launched from the KiCad Manager is generally better because:

- Cvpcb needs the project config file to know the footprint libraries to load.
- Cvpcb initializes the components footprint fields of the current schematic project. This is possible only if the project file is in the same path as the open schematic.

Launching CvPcb from an Eeschema launched from the KiCad manager assures automatically all this.

Warning

You actually **can** launch CvPcb from a stand alone Eeschema session though, but please note that any schematic opened that does not have a project file in the same path may be missing components due to missing libraries which will not show up in CvPcb. If there is no fp-lib-table file in the same path as the open schematic, no project specific footprint libraries will be available either.

2 CvPcb Features

2.1 Manual or Automatic Association

CvPcb allows for interactive assignment (manual) as well as automatic assignment via equivalence files.

3 Invoking CvPcb

CvPcb is only invoked from the schematic capture program Eeschema, by the tool:



Eeschema automatically passes the correct data (component list and footprints) to CvPcb. There is no update to do (unless some new components are not yet annotated), just run Cvpcb.

4 CvPcb Commands

4.1 Main Screen

The image below shows the main window of CvPcb.

Cvpcb (2015-06-07 BZR 5718)-p	roduct	Project: 'F:\tm	p\interf_u\interf_u.pro'		
File Preferences Help					
🖄 🖄 🔯 + 🜩	\$⇒ <mark>0</mark>	≱xi]			
Choke_SMD	1	BUS1 -	BUSPC :	1 capacitors:cnp_3mm_disc 🔺	
Resistors_SMD	2	C1 -	47µF : discret:CP6	2 capacitors:cnp_6mm_disc	
Socket_Strips	3	C2 -	47pF : discret:C1	3 capacitors:cnp_7.5mm_disc	
Sockets	4	C3 -	47pF : discret:C1	4 capacitors:cnp_7x2mm	
capacitors	5	C4 -	47uF : discret:CP6	5 capacitors:cnp_7x4.5mm	
connect	6	C5 -	47uF : discret:CP6	6 capacitors:cnp_7x6mm	
dip_sockets	7	C6 -	47uF : discret:CP6	7 capacitors:cnp_12mm_disc	
discret	8	D1 -	LED : discret:LEDV	8 capacitors:cnp_13x4mm	
display	9	D2 -	LED : discret:LEDV	9 capacitors:cnp_18x5mm	
inductors	10	JP1 -	CONN_8X2 : pin_array:pin_array_8x2	10 capacitors:cnp_18x6mm	
led	11	P1 -	DB25FEMELLE : connect:DB25FC	11 capacitors:cnp_18x8mm	
lib_smd	12	R1 -	100K : discret:R3	12 capacitors:CPA_5x11MM	
pin_array	13	R2 -	1K : discret:R3	13 capacitors:CPA_6x11MM	
smd_crystal_and_oscillator	14	R3 -	10K : discret:R3	14 capacitors:CPA_8x21MM	
smd_ssop_packages	15	R4 -	330 : discret:R3	15 capacitors:CPA_13x31MM	
smd_transistors	16	R5 -	330 : discret:R3	16 capacitors:CPA_16x31MM	
	17	RR1 -	9x1K : discret:r_pack9	17 capacitors:CPA_16x40MM	
	18	U1 -	74LS245 : dip_sockets:DIP-20300	18 capacitors:CP_5x6mm	
	19	U2 -	74LS688 : dip_sockets:DIP-20300	19 capacitors:CP_5x11mm	
	20	U3 -	74LS541 : dip_sockets:DIP-20_300	20 capacitors:CP_8x13mm	
	21	U5 -	628128 : dip_sockets:DIP-32600	21 capacitors:CP_8x21mm	
	22	U8 -	EP600 : dip_sockets:DIP-24300	22 capacitors:CP_10x13mm	
	23	U9 -	4003APG120 : Sockets:PGA120	23 capacitors:CP_10x16mm	
	24	X1 -	8MHz : discret:HC-18UH	24 capacitors:CP_10x21mm	
				25 capacitors:CP_10x25mm	
				26 capacitors:CP_13x21mm	
				27 capacitors:CP_13x25mm	
I ►					
Description: Capacitor, pol, cyl 5x6mm		Key w	ords: No f	filtering: 733	

The left pane contains the list of available footprint library file names associated with the project. The center pane contains the list of components loaded from the net list file. The right pane contains the list of available footprints loaded from the project footprint libraries. The component pane will be empty if no netlist file has been loaded and the footprint pane can be also empty if no footprint libraries are found.

4.2 Main Window Toolbar



The top toolbar allows for easy access to the following commands:

	Transfer the current footprint association to Eeschema (this is the content of footprint fields).
\$ \$	Invoke the CvPcb configuration menu.
D	Display the footprint of the component selected in the footprint window.
	Automatically select the previous component in the list without a footprint association.
-	Automatically select the next component in the list without a footprint association.
\$⇒ů	Automatically associate footprints with components starting using an equivalence file.
\$ %	Delete all footprint assignments.
PDF 人	Open the selected footprint documentation pdf file using the default pdf viewer.
	Enable or disable the filtering to limit the list of footprints to the footprint filters of the selected component.
##	Enable or disable the filtering to limit the list of footprints using the pin count of the selected component.
	Enable or disable filtering to limit the list of footprints using the selected library.

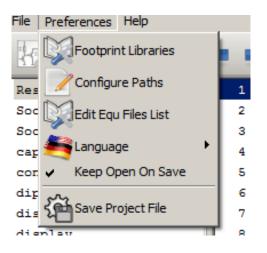
4.3 Main Window Keyboard Commands

The following table lists the keyboard commands for the main window:

Right Arrow	Activate the next pane to the right of the currently activated pane.
/ Tab	Wrap around to the first pane if the last pane is currently activated.
Left Arrow Activate the next pane to the left of the currently activated p	
	around to the last pane if the first pane is currently activated.
Up Arrow	Select the previous item of the currently selected list.
Down Arrow	Select the next item of the currently selected list.

Page Up	Select the item up one full page of the currently selected list.
Page Down	Select the item down one full page of the currently selected list.
Home	Select the first item of the currently selected list.
End	Select the last item of the currently selected list.

4.4 CvPcb Configuration



CvPcb can be automatically closed after saving the footprint association file, or not.

Invoking the "Libraries" entry in the "Preferences" menu displays the library configuration dialog.

Depending on the CvPcb version, there are 2 different methods of library management:

- The legacy management, using *.mod files, and a library list of files.
- The new "Pretty" format, using one file by footprint. It uses a folder list. Each folder (*.pretty folder name) is a library. When using this new method of library management, You can also use native libraries originating from GEDA/GPCB or even Eagle xml format files.

5 Footprint Libraries Management

5.1 Important remark:

This section is relevant only for KiCad versions since December 2013

5.2 Footprint Library tables

Since December 2013, Pcbnew and CvPcb uses a new library management tool based on *footprint library tables* which allows **direct use of footprint libraries** from

- KiCad Legacy footprint libraries (.mod files)
- KiCad New .*pretty* footprint libraries (on your local disk) (folders with .pretty extension, containing .kicad_mod files)
- KiCad New .*pretty* footprint libraries (on our Github server, or other Github server)
- GEDA libraries (folders containing .fp files)
- Eagle footprint libraries

Note

- you can write only KiCad .pretty footprint library folders on your local disk (and the .kicad_mod files inside these folders).
- All other formats are read only.

ble:	C: \Users \jpc \AppData \Roar	ning\kicad\fp-lib-table			
	Nickname	Library Path	Plugin Type	Options	Description
1	capacitors	\${KISYSMOD}/capacitors.pretty	KiCad 💌		
2	connect	\${KISYSMOD}/connect.pretty	KiCad		
3	dip_sockets	\${KISYSMOD}/dip_sockets.pretty	Github		
4	discret	\${KISYSMOD}/discret.pretty	Legacy Eagle		
5	display	\${KISYSMOD}/display.pretty	Geda-PCB		
6	inductors	\${KISYSMOD}/inductors.pretty	KiCad		
7	led	\${KISYSMOD}/led.pretty	KiCad		
8	lib_smd	\${KISYSMOD}/lib_smd.pretty	KiCad		
9	pin_array	\${KISYSMOD}/pin_array.pretty	KiCad		
10	smd_crystal_and_oscillator	\${KISYSMOD}/smd_crystal_and_oscillator.pretty	KiCad		
11	smd_ssop_packages	\${KISYSMOD}/smd_ssop_packages.pretty	KiCad		
12	smd_transistors	\${KISYSMOD}/smd_transistors.pretty	KiCad		
13	Resistors_SMD	\${KISYSMOD}/Resistors_SMD.pretty	KiCad KiCad KiCad		
14	Socket_Strips	\${KISYSMOD}/Socket_Strips.pretty			
15	Sockets	\${KISYSMOD}/Sockets.pretty			
16	Choke_SMD	\$(KIGITHUB)/Choke_SMD.pretty	Github		
Slob	al Libraries Project Speci	fic Libraries			
	Append with Wizard	Append Library Remove Library Move U	p Move Do	wn Optio	ns Editor
th Su	bstitutions				
	Environment Variable	Path Segment			
1	KIPRJMOD	F:\tmp\interf_u			
2		E: \kicad \share \kicad \modules \packages3d			
3		E: \kicad\share \kicad\modules			

The footprint library table is used to map a footprint library of any supported library type to a library nickname. **This nickname is used to look up footprints** instead of the previous method which depended on library search path ordering.

This allows CvPcb to access footprints with the same name in different libraries by ensuring that the correct footprint is loaded from the appropriate library. It also allows CvPcb to support loading libraries from different PCB editors such as Eagle and GEDA.

5.2.1 Global Footprint Library Table

The global footprint library table contains the list of libraries that are always available regardless of the currently loaded project file. The table is saved in the file fp-lib-table in the user's home folder. The location of this folder is dependent upon the operating system being used.

5.2.2 Project Specific Footprint Library Table

The project specific footprint library table contains the list of libraries that are available specifically for the currently loaded project file. The project specific footprint library table can only be edited when it is loaded along with the

project netlist file. If no project file is loaded or there is no footprint library table file in the project path, an empty table is created which can be edited and later saved along with the footprint assignment file.

5.2.3 Initial Configuration

The first time Pcbnew or CvPcb is run and the global footprint table file **fp-lib-table** is not found in the user's home folder, Pcbnew or CvPcb will attempt to copy the default footprint table file fp-lib-table stored in the system's KiCad template folder to the file fp-lib-table in the user's home folder.

If fp-lib-table cannot be found, an empty footprint library table will be created in the user's home folder. If this happens, the user can either copy fp-lib-table manually or configure the table by hand.

The default footprint library table includes many of the standard footprint libraries that are installed as part of KiCad.

Obviously, the **first thing** to do is to modify this table (add/remove entries) according to your work and the libraries you need for all your projects.

(Too many libraries to load is time consuming)

5.2.4 Adding Table Entries

In order to use a footprint library, it must first be added to either the global table or the project specific table. The project specific table is only applicable when you have a net list file open.

Each library entry must have a unique nickname.

This does not have to be related in any way to the actual library file name or path. The colon : character cannot be used anywhere in the nickname. Each library entry must have a valid path and/or file name depending on the type of library. Paths can be defined as absolute, relative, or by environment variable substitution (see section below).

The appropriate plug in type must be selected in order for the library to be properly read. KiCad currently supports reading KiCad legacy, KiCad Pretty, Eagle, and GEDA footprint libraries.

There is also a description field to add a description of the library entry. The option field is not used at this time so adding options will have no effect when loading libraries.

- Please note that you cannot have duplicate library nicknames in the same table. However, you can have duplicate library nicknames in both the global and project specific footprint library table.
- The project specific table entry will take precedence over the global table entry when duplicated names occur. When entries are defined in the project specific table, an fp-lib-table file containing the entries will be written into the folder of the currently open net list.

5.2.5 Environment Variable Substitution

One of the most powerful features of the footprint library table is environment variable substitution. This allows you to define custom paths to where your libraries are stored in environment variables. Environment variable substitution is supported by using the syntax $\{ENV_VAR_NAME\}$ in the footprint library path.

By default, at run time KiCad defines two environment variables:

- the KIPRJMOD environment variable. This always points to the current project directory and cannot be modified.
- the **KISYSMOD** environment variable. This points to where the default footprint libraries that were installed with KiCad are located.

You can override KISYSMOD by defining it yourself in preferences/Configure Path which allows you to substitute your own libraries in place of the default KiCad footprint libraries.

When a project netlist file is loaded, CvPcb defines the KIPRJMOD using the file path (the project path).

Pcbnew also defines this environment variable when loading a board file.

This allows you to store libraries in the project path without having to define the absolute path (which is not always known) to the library in the project specific footprint library table.

5.2.6 Using the GitHub Plugin

The GitHub is a special plugin that provides an interface for read only access to a remote Git Hub repository consisting of pretty (Pretty is name of the KiCad footprint file format) footprints and optionally provides "Copy On Write" (COW) support for editing footprints read from the GitHub repo and saving them locally. Therefore the "Git Hub" plugin is for **read only accessing remote pretty footprint libraries at** https://github.com. To add a GitHub entry to the footprint library table the "Library Path" in the footprint library table row must be set to a valid GitHub URL.

For example:

https://github.com/liftoff-sr/pretty_footprints

or

https://github.com/KiCad

Typically GitHub URLs take the form:

https://github.com/user_name/repo_name

The "Plugin Type" must be set to "Github". To enable the "Copy On Write" feature the option **allow_pretty_writing_to**_ must be added to the "Options" setting of the footprint library table entry. This option is the "Library Path" for local storage of modified copies of footprints read from the GitHub repo. The footprints saved to this path are combined with the read only part of the Git Hub repository to create the footprint library. If this option is missing, then the Git Hub library is read only. If the option is present for a Git Hub library, then any writes to this hybrid library will go to the local *.pretty directory. Note that the github.com resident portion of this hybrid COW library is always read only, meaning you cannot delete anything or modify any footprint in the specified Git Hub repository directly. The aggregate library type remains "Github" in all further discussions, but it consists of both the local read/write portion and the remote read only portion.

The table below shows a footprint library table entry without the option $allow_pretty_writing_to_this_dir$:

Nicknam	e Library Path	Plugin	Options	Descript.
		Type		
github	https://github.com/liftoff-sr/-	Github		Liftoff' s
	pretty_footprints			GH foot-
				prints

The table below shows a footprint library table entry with the COW option given. Note the use of the environment variable \${HOME} as an example only. The github.pretty directory is located in \${HOME}/pretty/ path. Anytime you use the option **allow_pretty_writing_to_this_dir**, you will need to create that directory manually in advance and it must end with the extension **.pretty**.

Nickname Library Path		Plugin	Options	Descript.
		Type		
github	https://github.com/liftoff-sr/-	Github	allow_pretty_writing_to_this_dir=	Liftoff' s
	pretty_footprints		{HOME}/pretty/github.pretty	GH foot-
				prints

Footprint loads will always give precedence to the local footprints found in the path given by the option **allow_pretty_writin** Once you have saved a footprint to the COW library's local directory by doing a footprint save in the footprint editor, no Git Hub updates will be seen when loading a footprint with the same name as one for which you' ve saved locally.

Always keep a separate local *.pretty directory for each Git Hub library, never combine them by referring to the same directory more than once.

Also, do not use the same COW (*.pretty) directory in a footprint library table entry. This would likely create a mess.

The value of the option **allow_pretty_writing_to_this_dir** will expand any environment variable using the \${} notation to create the path in the same way as the "Library Path" setting.

What is the point of COW? It is to turbo-charge the sharing of footprints.

If you periodically email your COW pretty footprint modifications to the GitHub repository maintainer, you can help update the GitHub copy. Simply email the individual *.kicad_mod files you find in your COW directories to the maintainer of the GitHub repository. After you have received confirmation that your changes have been committed, you can safely delete your COW file(s) and the updated footprint from the read only part of GitHub library will flow down. Your goal should be to keep the COW file set as small as possible by contributing frequently to the shared master copies at https://github.com.

5.2.7 Usage Patterns

Footprint libraries can be defined either globally or specifically to the currently loaded project. Footprint libraries defined in the user's global table are always available and are stored in the fp-lib-table file in the user's home folder.

Global footprint libraries can always be accessed even when there is no project net list file opened.

The project specific footprint table is active only for the currently open net list file.

The project specific footprint library table is saved in the file fp-lib-table in the path of the currently open net list . You are free to define libraries in either table. There are advantages and disadvantages to each method. You can define all of your libraries in the global table which means they will always be available when you need them. The disadvantage of this is that you may have to search through a lot of libraries to find the footprint you are looking for. You can define all your libraries on a project specific basis.

The advantage of this is that you only need to define the libraries you actually need for the project which cuts down on searching.

The disadvantage is that you always have to remember to add each footprint library that you need for every project. You can also define footprint libraries both globally and project specifically.

One usage pattern would be to define your most commonly used libraries globally and the library only required for the project in the project specific library table. There is no restriction on how you define your libraries.

5.3 Using the Footprint Library Table Wizard

A wizard to add footprint libraries to the footprint library tables is available from the *footprint library table editing* dialog.

Note also libraries can be any type of footprint library supported by KiCad.

It can add "local" libraries or libraries from a Github repository.

When libraries are on a Github repository, they can be added as remote libraries, or **downloaded and added as** *local libraries*.

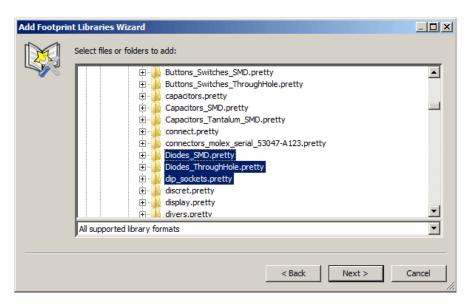
Here, the local libraries option is selected.

Add Footpri	nt Libraries Wizard		
	Welcome to the Add F	potprint Libraries Wizard!	
	Please select the source	e for the libraries to add:	
	Files on my comput	er	
	C Github repository	https://github.com/KiCad	
		Save a local copy to:	
		E:\kicad\share\kicad\modules	Browse
	<u>Visit our c</u>	fficial Kicad repository on Github and get more libra	ries
		< Back	Next > Cancel

Here, the remote libraries option is selected.

Add Footpri	nt Libraries Wizard		<u>- 0 ×</u>
	Welcome to the Add F	iootprint Libraries Wizard!	
	Please select the sour	ce for the libraries to add:	-
	C Files on my compu	ter	
	 Github repository 	https://github.com/KiCad	
		Save a local copy to:	
		E: \kicad\share \kicad \modules	Browse
	<u>Visit our o</u>	official Kicad repository on Github and get more libraries	
		< Back. Next >	Cancel

Depending on the selected option, one of these pages will be displayed, to select a list of libraries: Here, the local libraries option was selected.



Here, the remote libraries option was selected.

Add Footpr	int Libraries Wizard	
	Select Github libraries to add:	
	Air_Coils_SML_NEOSID.pretty	
.	Buttons_Switches_SMD.pretty	
	Buttons Switches ThroughHole.pretty	
	Buzzers Beepers.pretty	
	Capacitors_Elko_ThroughHole.pretty	
	Capacitors SMD.pretty	
	Capacitors_Tantalum_SMD.pretty	
4	Capacitors_ThroughHole.pretty	
	Choke Axial ThroughHole.pretty	
	Choke Common-Mode Wurth.pretty	
	Choke_Radial_ThroughHole.pretty	
	Choke SMD.pretty	
	Choke Toroid ThroughHole.pretty	
	Connect.pretty	
	Connectors_Molex.pretty	
	Converters DCDC ACDC.pretty	-
	Select all Unselect all Q	
		[
	< Back Next >	Cancel

After a set of libraries is selected, the next page validates the choice:

Library Diodes_SMD	OK Status	Format KiCad
Diodes_ThroughHole	OK	KiCad
dip_sockets	OK	KiCad

If some selected libraries are incorrect (not supported, not a footprint library $\cdots)$ they will be flagged as "INVALID" .

The last choice is the footprint library table to populate:

- the global table
- the local table (the project specific table)

Add Footpri	nt Libraries Wizard	
	Where do you wish the new libraries to be added: Image: To global library configuration (visible by all projects)) Image: To the current project only	
	< Back Finish	Cancel

6 Viewing the Current Footprint

6.1 The view footprint command

The view footprint command displays the footprint currently selected in the *footprint* window. A 3D model of the component can be shown if it has been created and assigned to the footprint. Below is the footprint viewer window.

1.	€	Q	、 (A	ß	ζ	00																													
										1				W	- F	RE 1	F *:	* . m	i.	nn	лР	ac	10	ł						•						I
~													- -						`		y' T		13													
ln																	Ó					9														
nm											Ļ										2															
																	Ù.)														ſ
Ø																K																				
T																1	5) 6)				्र दु															
Zí																×.	X				K	X														
																	Ľ																			
	•																	III																		Þ
REF* [*] DIP-1	* .4_W7	.62m	ım_	Lon	qPa	ds		.ast Sep		-		N	leti	ist	Pa	th			ay <mark>e</mark> r Cu			Pad: 14	s		St	atu	s	An 0.1	igle D			trik prm	iute ial	28	Fc Hi	oot ou:

6.1.1 Status Bar Information

The status bar is located at the bottom of the CvPcb new main window and provides useful information to the user. The following table defines the contents of each pane in the status bar.

Left	Component count: total, unassigned
Middle	Filter list of the selected component
Right	Filtering mode and count of available footprints

6.1.2 Keyboard Commands

F1	Zoom In
F2	Zoom Out
F3	Refresh Display
F4	Move cursor to center of display window
Home	Fit footprint into display window
Space Bar	Set relative coordinates to the current cursor position
Right Arrow	Move cursor right one grid position

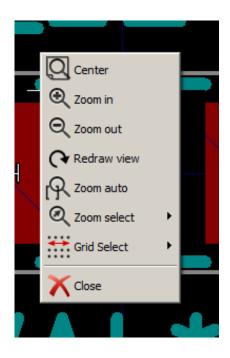
Left Arrow	Move cursor left one grid position
Up Arrow	Move cursor up one grid position
Down Arrow	Move cursor down one grid position

6.1.3 Mouse Commands

Scroll Wheel	Zoom in and out at the current cursor position
Ctrl + Scroll Wheel	Pan right and left
Shift + Scroll Wheel	Pan up and down
Right Button Click	Open context menu

6.1.4 Context Menu

Displayed by right-clicking the mouse:



Zoom Selection (Select Zoom)	Direct selection of the display zoom.
Grid Selection (Grid Select)	Direct selection of the grid.

6.1.5 Horizontal Toolbar

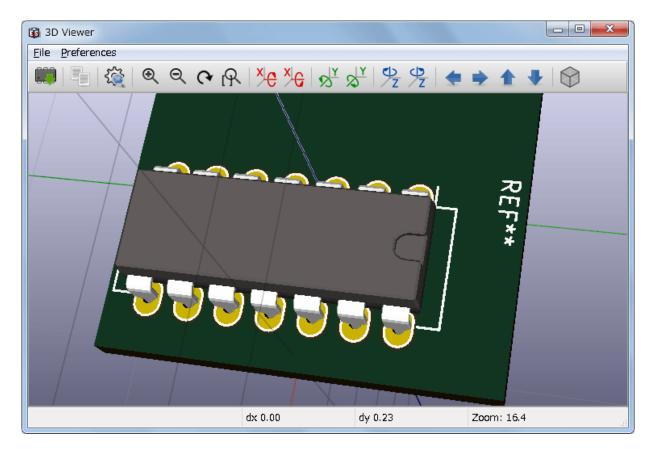
*	Show display options dialog
Ð,	Zoom in
Q	Zoom out
C+	Redraw

Q	Fit drawing in display area
	Open 3D model viewer

6.1.6 Vertical Toolbar

	Show or hide the grid
↓ r	Show coordinates in polar or rectangular notation
in	Display coordinates in inches
mm	Display coordinates in millimeters
	Toggle pointer style
ø	Toggle between drawing pads in sketch or normal mode
T	Toggle between drawing text in sketch or normal mode
Ø	Toggle between drawing edges in sketch or normal mode

6.2 Viewing the Current 3D Model



6.2.1 Mouse Commands

Scroll Wheel	Zoom in and out at the current cursor position
Ctrl + Scroll Wheel	Pan right and left
Shift + Scroll Wheel	Pan up and down

6.2.2 Horizontal Toolbar

	Reload the 3D model						
	Copy 3D image to clipboard						
ξζ ε	Set 3D viewer options						
⊕ `	Zoom in						
Θ ,	Zoom out						
C+	Redraw						
Q	Fit drawing in display area						
×e	Rotate backward along the X axis						
×G	Rotate forward along the X axis						
1	Rotate backward along the Y axis						
Ø ^Ÿ	Rotate forward along the Y axis						
Ŷz	Rotate backward along the Z axis						
 У 	Rotate forward along the Z axis						
\	Pan left						
	Pan right						
	Pan up						
₽	Pan down						
\bigcirc	Toggle orthographic projection mode on and off						

7 Using CvPcb to Associate Components with Footprints

7.1 Manually Associating Footprints with Components

To manually associate a footprint with a component first select a component in the component pane. Then select a footprint in the footprint pane by double-clicking the left mouse button on the name of the desired footprint. The unassigned next component in the list is automatically selected. Changing the component footprint is performed in the same manner.

7.2 Filtering the Footprint List

If the selected component and/or library is highlighted when the one or more of the filtering options is enabled, the displayed footprint list in CvPcb is filtered accordingly.

The icons $\mathbf{E} = \mathbf{E} \cdot \mathbf{E}$ enable and disable the filtering feature. When the filtering is not enabled, the full footprint list is shown.

Without filtering:

The second secon					
File Preferences Help					
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Choke_SMD	1	BUS1 -	BUSPC :	154 connect:SIL-15	
Resistors_SMD	2	C1 -	47µF : discret:CP6	155 connect:SIL-18	
Socket_Strips	3	C2 -	47pF : discret:C1	156 connect:SIL-26	
Sockets	4	C3 -	47pF : discret:C1	157 connect:SOC676BR	
capacitors	5	C4 -	47uF : discret:CP6	158 connect:SOC676C	
connect	6	C5 -	47uF : discret:CP6	159 connect:SOC676NY	
dip_sockets	7	C6 -	47uF : discret:CP6	160 connect:TESTPOINT	
discret	8	D1 -	LED : discret:LEDV	161 connect:USB_A	
display	9	D2 -	LED : discret:LEDV	162 connect:USB_B	
inductors	10	JP1 -	CONN_8X2 : pin_array:pin_array_8x2	163 connect:USB_MINI_B	
led	11	P1 -	DB25FEMELLE : connect:DB25FC	164 connect:VASCH5x2	
lib_smd	12	R1 -	100K : discret:R3	165 connect:VASCH8x2	
pin_array	13	R2 -	1K : discret:R3	166 connect:VASCH10x2	
smd_crystal_and_oscillator	14	R3 -	10K : discret:R3	167 connect:x3d_test_molex	
smd_ssop_packages	15	R4 -	330 : discret:R3	168 connect:XLR3MC	
smd_transistors	16	R5 -	330 : discret:R3	169 connect:XLR5MC	
	17	RR1 -	9x1K : discret:r_pack9	170 dip_sockets:DIP-6_300	
	18	U1 -	74LS245 : dip_sockets:DIP-20300	171 dip_sockets:DIP-6_300_EI	
	19	U2 -	74LS688 : dip_sockets:DIP-20300	172 dip_sockets:DIP-8_300	
	20	U3 -	74LS541 : dip_sockets:DIP-20300	173 dip_sockets:DIP-8_300_EI	
	21	U5 -	628128 : dip_sockets:DIP-32600	174 dip_sockets:DIP-10_300	
	22	U8 -	EP600 : dip_sockets:DIP-24300	175 dip_sockets:DIP-10_300_F	
	23	U9 -	4003APG120 : Sockets:PGA120	176 dip_sockets:DIP-14_300	
	24	X1 -	8MHz : discret:HC-18UH	177 dip_sockets:DIP-14_300_E	
				178 dip_sockets:DIP-16_300	
				179 dip_sockets:DIP-16300_F	
				180 dip_sockets:DIP-18_300	
Description: Capacitor, pol, cyl 5x6mm		Key w	ords: No filt	ering: 733	

Filtered by list of footprint filters assigned to the selected component. The component filters are listed on the center pane of the status bar at the bottom of the main window.

Filtered by the footprint filter of the selected component:

🖏 Cvpcb (2015-06-07 BZR 5718)-product Project: 'F:\tmp\interf_u\interf_u.pro'						
File Preferences Help						
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Choke_SMD	1	BUS1 -	BUSPC :	1 discret:LEDV		
Resistors_SMD	2	C1 -	47µF : discret:CP6	2 led:LED-3MM		
Socket_Strips	3	C2 -	47pF : discret:C1	3 led:LED-5MM		
Sockets	4	СЗ -	47pF : discret:C1	4 led:LED-0603		
capacitors	5	C4 -	47uF : discret:CP6	5 led:LED-0805		
connect	6	C5 -	47uF : discret:CP6	6 led:LED-1206		
dip_sockets	7	C6 -	47uF : discret:CP6			
discret	8	D1 -	LED : discret:LEDV			
display	9	D2 -	LED : discret:LEDV			
inductors	10	JP1 -	CONN_8X2 : pin_array:pin_array_8x2			
led	11	P1 -	DB25FEMELLE : connect:DB25FC			
lib_smd	12	R1 -	100K : discret:R3			
pin_array	13	R2 -	1K : discret:R3			
smd_crystal_and_oscillator	14	R3 -	10K : discret:R3			
smd_ssop_packages	15	R4 -	330 : discret:R3			
smd_transistors	16	R5 -	330 : discret:R3			
	17	RR1 -	9x1K : discret:r_pack9			
	18	U1 -	74LS245 : dip_sockets:DIP-20_300			
	19	U2 -	74LS688 : dip_sockets:DIP-20_300			
	20	U3 -	74LS541 : dip_sockets:DIP-20_300			
	21	US -	628128 : dip_sockets:DIP-32600			
	22	U8 -	EP600 : dip_sockets:DIP-24300			
	23	U9 -	4003APG120 : Sockets:PGA120			
	24	X1 -	8MHz : discret:HC-18UH			
▼						
Components: 24, unassigned: 1		Filter	ist: LED-3MM, LED-5MM, LED-10MM, LED-0603, LE Filtered	by key words: 6		

In the component library editor in Eeschema, the footprint list was set using the entries in the footprint filter tab of the component properties dialog as shown below.

Properties for R	X
Options Description Alias Footprint Filter	
Footprints R? SM0603 SM0805 R?-*	Add Delete Delete All
ОК	Cancel

Filtered by the pin count of the selected component:

The Preferences Help						
🖄 🕸 🔯 🔙 !		⇒ į́ ≱×į́				
Choke_SMD	1	BUS1 -	BUSPC :		1 connect:GS_8x2	
Resistors_SMD	2	C1 -	47µF : discr	et:CP6	2 connect:he10-14c	
Socket_Strips	3	C2 -	47pF : discr	et:C1	3 connect:he10-14d	
Sockets	4	C3 -	47pF : discr	et:C1	4 connect:RJ45_TRANSFO	
capacitors	5	C4 -	47uF : discr	et:CP6	5 connect:VASCH8x2	
connect	6	C5 -	47uF : discr	et:CP6	6 dip_sockets:DIP-16_300	
dip_sockets	7	C6 -	47uF : discr	et:CP6	7 dip_sockets:DIP-16_300_ELI	5
discret	8	D1 -	LED : discr	et:LEDV	8 discret:SWDIP8	
display	9	D2 -	LED : discr	et:LEDV	9 display:AFF_2x7SEG-DIGIT_10)mm
inductors	10	JP1 -	CONN_8X2 : pin_a	rray:pin_array_8x2	10 display:Dx04	
led	11	P1 -	DB25FEMELLE : conne	ct:DB25FC	11 lib_smd:SO16E	
lib_smd	12	R1 -	100K : discr	et:R3	12 lib_smd:SO16L	
pin_array	13	R2 -	1K : discr	et:R3	13 lib_smd:SO16N	
smd_crystal_and_oscilla	14	R3 -	10K : discr	et:R3	14 lib_smd:SO16W	
smd_ssop_packages	15	R4 -	330 : discr	et:R3	15 lib_smd:TSSOP16	
smd_transistors	16	R5 -	330 : discr	et:R3	16 pin_array:pin_array_2x08	
	17	RR1 -	9x1K : discr	et:r_pack9	17 Resistors_SMD:R_Cat16-8	
	18	U1 -	74LS245 : dip_s	ockets:DIP-20300	18 smd_ssop_packages:SSOP-16	
	19	U2 -	74LS688 : dip_s	ockets:DIP-20300	19 Socket_Strips:Socket_Strip	Angl
	20	U3 -	74LS541 : dip_s	ockets:DIP-20300	20 Socket_Strips:Socket_Strip	Angl
	21	US -	628128 : dip_s	ockets:DIP-32_600	21 Socket_Strips:Socket_Strip	Stra
	22	U8 -	EP600 : dip_s	ockets:DIP-24300	22 Socket_Strips:Socket_Strip	Stra
	23	U9 -	4003APG120 : Socke	ts:PGA120		
	24	X1 -	8MHz : discr	et:HC-18UH		
۰ ۲	•			<u>ا</u>		F
omponents: 24, unassigned: 1		Fi	lter list:	F	Filtered by pin count: 22	1

Filtered by the selected library.

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Choke_SMD	1	BUS1 -	BUSPC :	1 pin_array:pin_array_1x01
Resistors_SMD	2	C1 -	47µF : discret:CP6	2 pin_array:pin_array_1x02
Socket_Strips	3	C2 -	47pF : discret:C1	3 pin_array:pin_array_1x03
Sockets	4	C3 -	47pF : discret:C1	4 pin_array:pin_array_1x04
capacitors	5	C4 -	47uF : discret:CP6	5 pin_array:pin_array_1x05
connect	6	C5 -	47uF : discret:CP6	6 pin_array:pin_array_1x06
dip_sockets	7	C6 -	47uF : discret:CP6	7 pin_array:pin_array_1x28
discret	8	D1 -	LED : discret:LEDV	8 pin_array:pin_array_2x02
display	9	D2 -	LED : discret:LEDV	9 pin_array:pin_array_2x03
inductors	10	JP1 -	CONN_8X2 : pin_array:pin_array_8x2	10 pin_array:pin_array_2x04
led	11	P1 -	DB25FEMELLE : connect:DB25FC	11 pin_array:pin_array_2x05
lib_smd	12	R1 -	100K : discret:R3	12 pin_array:pin_array_2x06
pin_array	13	R2 -	1K : discret:R3	13 pin_array:pin_array_2x08
smd_crystal_and_oscillator	14	R3 -	10K : discret:R3	14 pin_array:pin_array_2x10
smd_ssop_packages	15	R4 -	330 : discret:R3	15 pin_array:pin_array_2x12
smd_transistors	16	R5 -	330 : discret:R3	16 pin_array:pin_array_2x13
	17	RR1 -	9x1K : discret:r_pack9	17 pin_array:pin_array_2x20
	18	U1 -	74LS245 : dip_sockets:DIP-20300	18 pin_array:pin_array_2x30
	19	U2 -	74LS688 : dip_sockets:DIP-20300	
	20	U3 -	74LS541 : dip_sockets:DIP-20300	
	21	U5 -	628128 : dip_sockets:DIP-32600	
	22	U8 -	EP600 : dip_sockets:DIP-24_300	
	23	U9 -	4003APG120 : Sockets:PGA120	
	24	X1 -	8MHz : discret:HC-18UH	

The filtering can be combined to form more complex filtering to help reduce the number of footprints in the footprint pane.

Filtered by the selected component pin count and the component filter:

Typeb (2015-06-07 BZR 5718)-product Project: 'F:\tmp\interf_u\interf_u.pro'				
File Preferences Help	≱⇒į̇́	≱x į́ ∠	888 888 888 a∰ a(#)	
Choke_SMD	1	BUS1 -	BUSPC :	1 connect:GS_8x2
Resistors_SMD	2	C1 -	47µF : discret:CP6	2 connect:he10-14c
Socket_Strips	3	C2 -	47pF : discret:C1	3 connect:he10-14d
Sockets	4	СЗ -	47pF : discret:C1	4 connect:RJ45_TRANSFO
capacitors	5	C4 -	47uF : discret:CP6	5 connect:VASCH8x2
connect	6	C5 -	47uF : discret:CP6	6 dip_sockets:DIP-16_300
dip_sockets	7	C6 -	47uF : discret:CP6	7 dip_sockets:DIP-16300_ELL
discret	8	D1 -	LED : discret:LEDV	8 discret:SWDIP8
display	9	D2 -	LED : discret:LEDV	9 display:AFF_2x7SEG-DIGIT_10
inductors	10	JP1 -	CONN_8X2 : pin_array:pin_array_8x2	10 display:Dx04
led	11	P1 -	DB25FEMELLE : connect:DB25FC	11 lib_smd:SO16E
lib_smd	12	R1 -	100K : discret:R3	12 lib_smd:SO16L
pin_array	13	R2 -	1K : discret:R3	13 lib_smd:SO16N
smd_crystal_and_oscillator	14	R3 -	10K : discret:R3	14 lib_smd:SO16W
smd_ssop_packages	15	R4 -	330 : discret:R3	15 lib_smd:TSSOP16
smd_transistors	16	R5 -	330 : discret:R3	16 pin_array:pin_array_2x08
	17	RR1 -	9x1K : discret:r_pack9	17 Resistors_SMD:R_Cat16-8
	18	U1 -	74LS245 : dip_sockets:DIP-20300	18 smd_ssop_packages:SSOP-16
	19	U2 -	74LS688 : dip_sockets:DIP-20300	19 Socket_Strips:Socket_Strip_
	20	U3 -	74LS541 : dip_sockets:DIP-20300	20 Socket_Strips:Socket_Strip_
	21	U5 -	628128 : dip_sockets:DIP-32600	21 Socket_Strips:Socket_Strip_
	22	U8 -	EP600 : dip_sockets:DIP-24300	22 Socket_Strips:Socket_Strip_
	23	U9 -	4003APG120 : Sockets:PGA120	
	24	X1 -	8MHz : discret:HC-18UH	
				K D
Components: 24, unassigned: 1			r list: Filtere	d by key words+pin count: 22

23 / 24

8 Automatic Associations

8.1 Equivalence files

Equivalence files allow for automatic assignment of footprints to components.

They list the name of the corresponding footprint according to the name (*value field*) of the component. These files typically have the **.equ** file extension.

They are plain text files and may be edited by using any plain text editor, and must be created by the user.

8.2 Equivalence File Format

Equivalence files consist of one line for each component. Each line has the following structure:

'component value' 'footprint name'

Each name must be single quoted by the ' character and the component and footprint names must be separated by one or more spaces.

Example:

If the U3 component is circuit 14011 and its footprint is 14DIP300, the line is:

'14011' '14DIP300'

Any line starting with # is a comment.

Here is an example equivalence file:

```
#integrated circuits (smd):
'74LV14' 'S014E'
'74HCT541M' 'SO20L'
'EL7242C' 'SO8E'
'DS1302N' 'SO8E'
'XRC3064' 'VQFP44'
'LM324N' 'S014E'
'LT3430' 'SSOP17'
'LM358' 'SO8E'
'LTC1878' 'MSOP8'
'24LC512I/SM' 'SO8E'
'LM2903M' 'SO8E'
'LT1129_S08' 'S08E'
'LT1129CS8-3.3' 'SO8E'
'LT1129CS8' 'SO8E'
'LM358M' 'SO8E'
'TL7702BID' 'SO8E'
'TL7702BCD' 'SO8E'
'U2270B' 'SO16E'
#Xilinx
'XC3S400PQ208' 'PQFP208'
```

```
'XCR3128-VQ100' 'VQFP100'
'XCF08P' 'BGA48'
#upro
'MCF5213-LQFP100' 'VQFP100'
#regulators
'LP2985LV' 'S0T23-5'
```

8.3 Automatically Associating Footprints to Components

Click on the automatic footprint association button on the top toolbar to process an equivalence file.

All components found by their value in the selected equivalence (*.equ) file will have their footprint automatically assigned.